

Remarks/Arguments

Claims 41, 43 and 47-50 have been amended. Claims 42 and 44-46 have been canceled.

Claims 41-50 have been rejected under 35 U.S.C. § 103(a) as being unpatentable over the Pfeiffer et al. patent. With respect to applicant's claims, as amended, this rejection is respectfully traversed.

Applicant's independent claims 41 and 47 have been amended to better define applicant's invention. Applicant's amended claim 41 now recites that the image capture unit permits the refresh control unit to refresh the memory in a blanking period, and that the image compression unit permits the refresh control unit to refresh the memory every time a predetermined time is passed. Similarly, amended claim 47 now requires that the image capture unit permits the refresh control unit to refresh the memory in a blanking period, and that the image processing unit permits the refresh control unit to refresh the memory every time a predetermined time is passed.

Such constructions are not believed to be taught or suggested by the Pfeiffer et al. patent. In particular, the Pfeiffer, et al. patent discusses at column 23, lines 35-47, the refreshing of the memory DRAMS and states that "the video DRAM refresh controller 232 is programmed to request DRAM refresh memory cycles on a periodic basis, as programmed by a refresh-interval register in the bus interface 240." The patent also states at lines 21-27 of column 23, that "the DRAM refresh controller 232 and the image algorithm process address interface port 234 operate asynchronously, and thus each circuit can request access of the image memory 82" and further that an "arbiter circuit is provided to determine the priority of various requests, and service such requests in accordance with a predetermined priority scheme."

Thus, in the Pfeiffer, et al. patent the programmed refresh controller controls refreshing of the memory 82 DRAMS and an arbiter arbitrates access to the memory. However, is not believed that the patent teaches or suggests an image capture unit which permits a refresh control unit to refresh a memory in a blanking period and an image compression unit which permits the refresh control unit to refresh the memory every time a predetermined time is passed, or an image capture unit which permits a refresh control unit to refresh a memory in a blanking period and an image processing unit which permits the refresh control unit to refresh the memory every time a predetermined time is passed. Applicant's amended claims 41 and 47, and their respective dependent claims, all of which recite one or the other of such features thus patentably distinguish over the cited Pfeiffer, et al. patent

In view of the above, it is submitted that applicant's claims, as amended patentably distinguish over the cited art of record. accordingly, reconsideration of the claims is respectfully requested.

Dated: September 4, 2003

Respectfully submitted,

ROBIN, BLECKER & DALEY
330 Madison Avenue
New York, NY 10017
T (212) 682-9640

John J. Torrente
John J. Torrente
Reg. No. 26,359
Attorney of Record

OFFICIAL

RECEIVED
CENTRAL FAX CENTER
SEP 04 2003